

**PROPOSED CLAIM AMENDMENT**

Please amend the claims in accordance with the following listing.

**Listing of Claims:**

1. (Original): A system for communication on a chip, comprising:  
an on-chip communication bus including plural tracks; and  
a plurality of stations that couple a plurality of on-chip components to the on-chip communication bus;  
wherein each station has a dedicated track which it can use to send information to other stations.
2. (Original): A system as in claim 1, wherein the stations use a packet based communication protocol.
3. (Original): A system as in claim 1, wherein the on-chip components include a PCI bridge, a USB component, or an inter-integrated-circuit component.
4. (Original): A system as in claim 1, wherein each station includes:  
an initiator that requests permission to transmit outgoing data over a track to another station and that transmits the outgoing data;  
an arbiter that evaluates requests from other stations and selects a track on which to receive incoming data; and  
a target that receives the incoming data.

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